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Internship “High Temp analog design”

Division	DESIGN
Department	BU Watch
Reporte à (fonction du supérieur) Reporting to (function of the manager)	BU Leader
Nb. de subordonnés Nr. of subordinates	-
Lieu de travail Place of employment	Marin
Mission globale Global mission	Analog Design on Advanced, High Temperature Clock and PowerManagement building blocks
Tâches et responsabilités Tasks and responsibilities	<ul style="list-style-type: none"> • Transfer our existing building blocks into EM's proprietary ALP011 technology node using state-of-art CAD environment. • Adapt this building blocks to work reliable up to 125°C • Work in close collaboration with the analog experts • Integration of the fully verified blocks on EM MPW shuttle • Bench top validation • Design documentation • Regular reviews with the design team
Profil requis Required profile	<ul style="list-style-type: none"> • Formation, compétences, expérience Education, background, experience • Connaissances linguistiques Language skills • Avantages bienvenus Welcome plus
Divers Misc.	<p>Engineer in Microelectronics with good analog design knowledge</p> <p>French or English</p>